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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Randy Benella et al. Art Unit :
Serial No. : 09/662,054 Examiner :
Filed : September 15, 2000
Title : DIGITAL SYSTEM OF ADJUSTING DELAYS ON CIRCUIT BOARDS

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information Disclosure Statement and documents listed on form PTO-1449.

Each enclosed document listed on the herewith Form PTO-1449 was cited in the attached PCT International Search Report dated December 30, 2002, issued in a foreign counterpart application not more than three months ago. To the best knowledge of the undersigned, this is the first citation of these items in any communication from a foreign patent office in a counterpart foreign application.

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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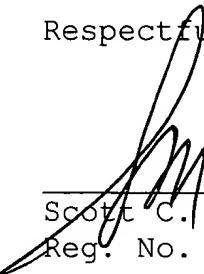
Each of the documents is either in the English language or includes an English language abstract, hence no concise explanation is necessary per Rule 98(a)(3).

Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: January 28, 2003



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